

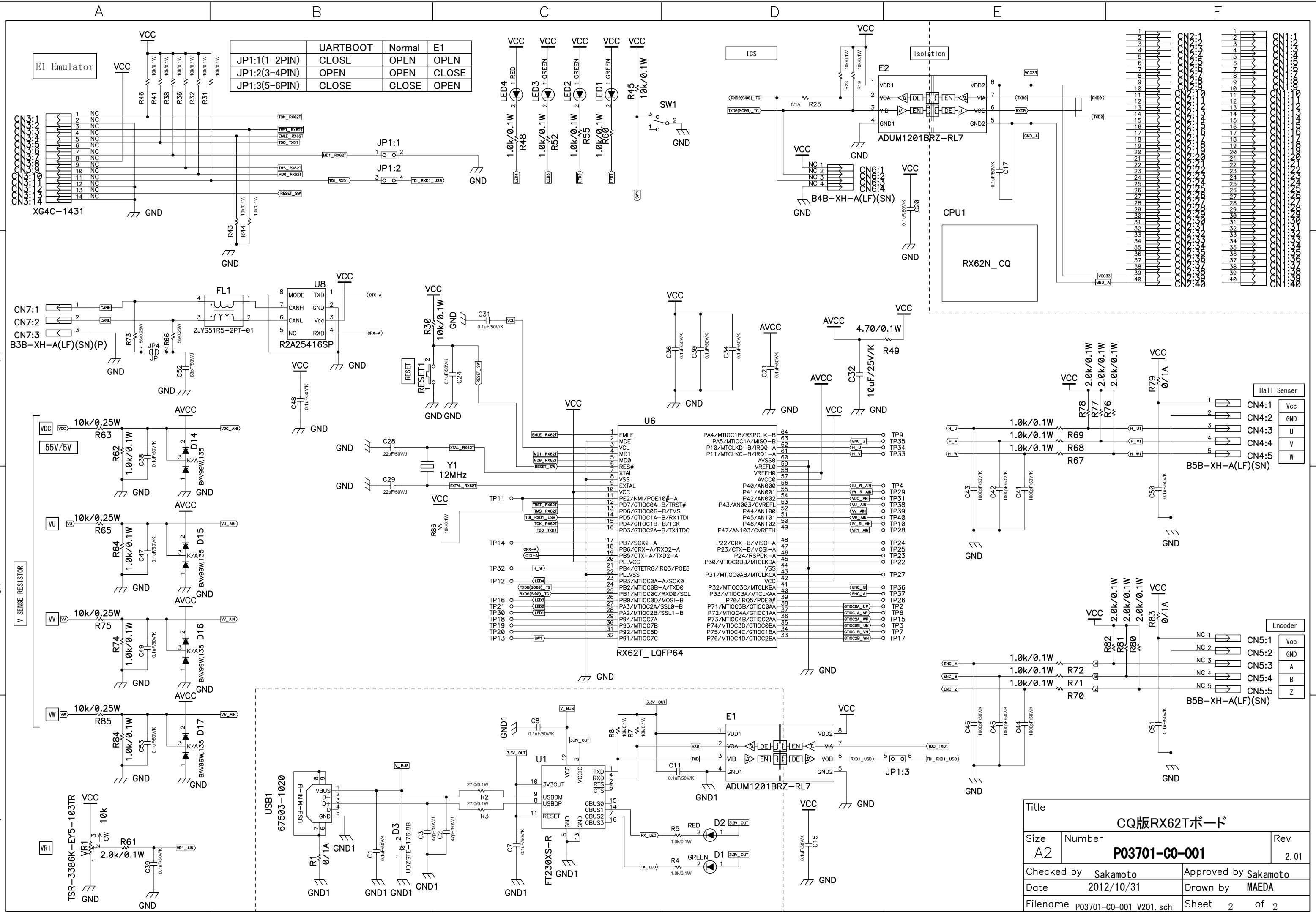
$$10[A] \times 0.05\Omega \times G \rightarrow G = 5$$

$$5 \times R59 / (R59 + R58) \times G = 2.5$$

$$(R59 + R58) / R59 = 2 \times G = 10$$

$$R59 = 220, R58 = 1.98k$$

Title			CQ版RX62Tボード		
Size	Number			Rev	
A2	P03701-C0-001			2.01	
Checked by	Sakamoto		Approved by	Sakamoto	
Date	2012/10/31		Drawn by	MAEDA	
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	UARTBOOT	Normal	E1
JP1:1(1-2PIN)	CLOSE	OPEN	OPEN
JP1:2(3-4PIN)	OPEN	OPEN	CLOSE
JP1:3(5-6PIN)	CLOSE	CLOSE	OPEN

Pin	Signal	Pin	Signal
1	EMLE	64	PA4/MTIOC1B/RSPCLK-B
2	MDF	63	PA5/MTIOC1A/MISO-B
3	VCC	62	P18/MTCLKD-B/IRQ0-A
4	MD1	61	P11/MTCLKC-B/IRQ1-A
5	MD0	60	AVSS0
6	RES#	59	VREFL0
7	XTAL	58	VREFH0
8	XTAL	57	VSS
9	EXTAL	56	AVCC0
10	VCC	55	P40/AN000
11	PE2/NMI/POE10#-A	54	P41/AN001
12	PD7/GTIOC0A-B/TRST#	53	P42/AN002
13	PD6/GTIOC0B-B/TMS	52	P43/AN003/CVREFL
14	PD5/GTIOC1A-B/RX1TDI	51	P44/AN100
15	PD4/GTIOC1B-B/TCK	50	P45/AN101
16	PD3/GTIOC2A-B/TX1TDO	49	P46/AN102
17	PB7/SCK2-A	48	P47/AN103/CVREFH
18	PB6/CRX-A/RXD2-A	47	P22/CRX-B/MISO-A
19	PB5/CTX-A/TXD2-A	46	P23/CTX-B/MOSI-A
20	PLLVC	45	P24/RSPCK-A
21	PB4/GTETRQ/IRQ3/POE8	44	P30/MTIOC8BB/MTCLKDA
22	PLLSS	43	VSS
23	PB3/MTIOC0A-A/SCK0	42	P31/MTIOC8AB/MTCLKCA
24	PB2/MTIOC0B-A/TXD0	41	VCC
25	PB1/MTIOC0C/RXD0/SCL	40	P32/MTIOC3C/MTCLKBA
26	PB0/MTIOC0D/MOSI-B	39	P33/MTIOC3A/MTCLKAA
27	PA3/MTIOC2A/SSL0-B	38	P70/IRQ5/POE0
28	PA2/MTIOC2B/SSL1-B	37	P71/MTIOC3B/GTIOC0AA
29	PA4/MTIOC7A	36	P72/MTIOC4A/GTIOC1AA
30	PA5/MTIOC7B	35	P73/MTIOC4B/GTIOC2AA
31	PA6/MTIOC6C	34	P74/MTIOC3D/GTIOC0BA
32	PA7/MTIOC6D	33	P75/MTIOC4C/GTIOC1BA
			P76/MTIOC4D/GTIOC2BA

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	Sheet	of
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